



DOCKET NO.: BUR920000228US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Malinowski *et al.*

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Examiner: Thai, Luan C.

Serial No.: 09/683,027

Art Unit: 2827

Filed: 11/9/01

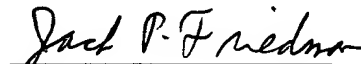
For: **DUAL CHIP STACK METHOD FOR ELECTRO-STATIC DISCHARGE
PROTECTION OF INTEGRATED CIRCUITS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Withdrawal of Petition Filed Under 37 C.F.R. §1.181(a)

Withdrawal of petition filed on November 10, 2003, under 37 C.F.R. §1.181(a) is being requested for the above-referenced matter, and concurrent with this request for withdrawing petition filed under 37 C.R.F. §1.181(a), Applicants are submitting herewith a Petition for Revival of an Application for Patent Abandoned Unintentionally to expedite prosecution. In an effort to expedite prosecution, Applicants' Representative made repeated efforts by telephone on April 12, 2005; May 2, 2005; June 13, 2005 and June 15, 2005, to request a timely decision on the petition filed under 37 C.F.R. §1.181(a) nearly two years ago. However, to date, the United States Patent and Trademark Office has not acted on this petition.

June 16, 2005


Jack P. Friedman
Reg. No. 44,688

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Latham, New York 12110
(518) 220-1850

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Petition Under 37 C.F.R. §1.181(a)

This Petition is in reference to the Notice of Abandonment mailed October 27, 2003.

Applicants request withdrawal of the holding of abandonment for the above-referenced matter.

Enclosed please find a copy of the Response filed on December 9, 2002 with confirmation that the Response was received by the United States Patent and Trademark Office. In addition, enclosed please find a Request for Continued Examination Transmittal.

Jack P. Friedman 4/10/2003
Jack P. Friedman
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(518) 220-1850



IBM-7468
UTILITY/DESIGN PATENT
(amend/final amend)

Date:

11/10/03

Rec'd in the U.S. Patent & Trademark Office on the date stamped hereon via

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Case #

BUR92000228US1

S/N:

09/683027

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Pat. #

Issued:

Examiner:

Thai, Luan C.

Art Unit:

0827

Batch:

Atty:

OPF

Applicant:

Malinowski et al

Title:

Dual Chip Stack Method for ...

☒ Transmittal in duplicate

☐ Mo. Ext. Time in dupe.

☐ Change of Address

☐ Request for Drawing Changes

☐ Small Entity Statement

☐ pgs. Drawings

☐ Assignment in pgs.

☐ \$ Check for Amend.

☐ \$ Check for Assig.

☐ Information Disclosure Statement; PTO-1449 in duplicate with Reference(s)

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